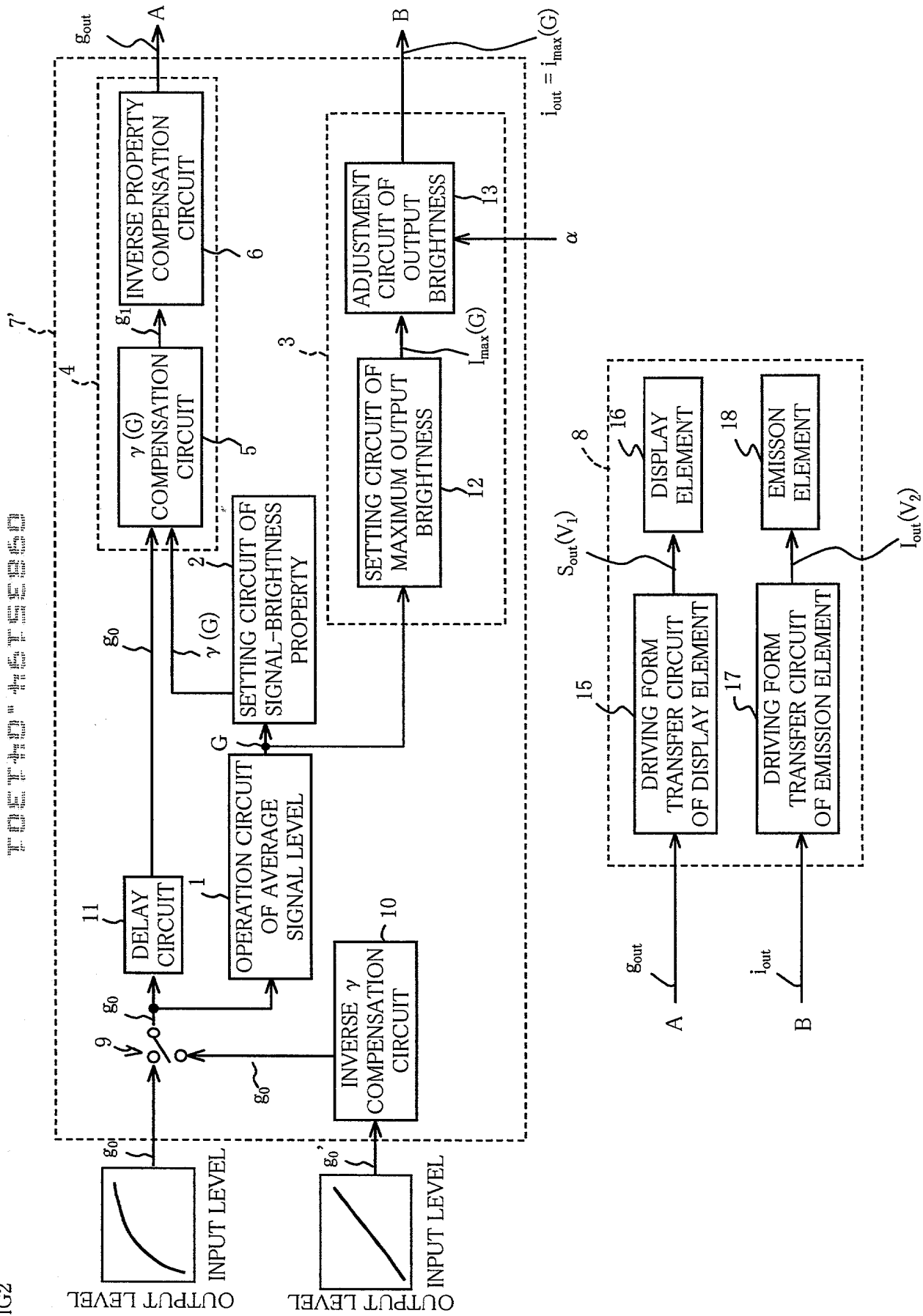


FIG2



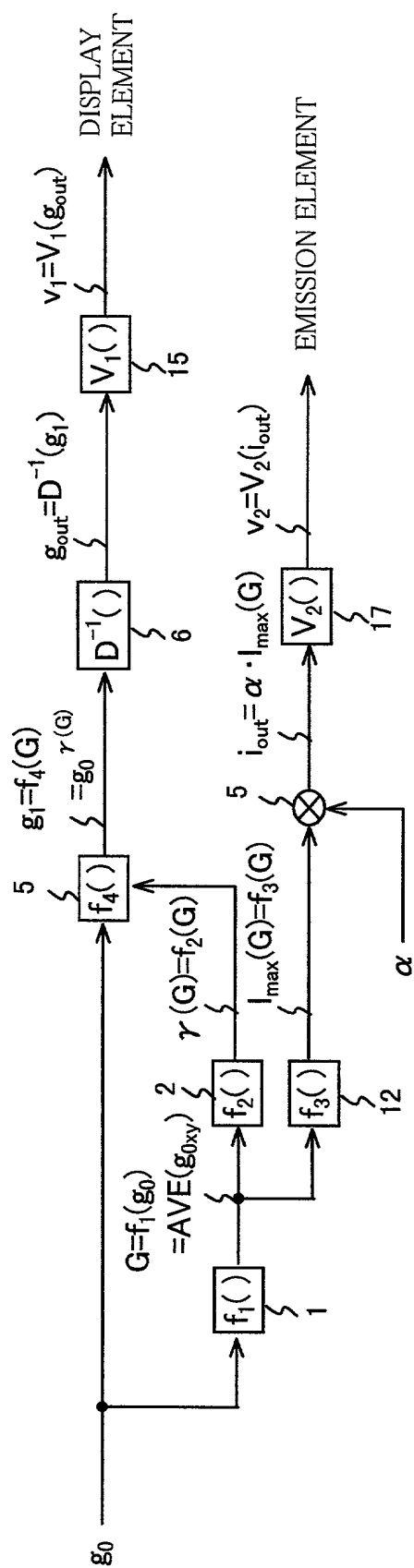
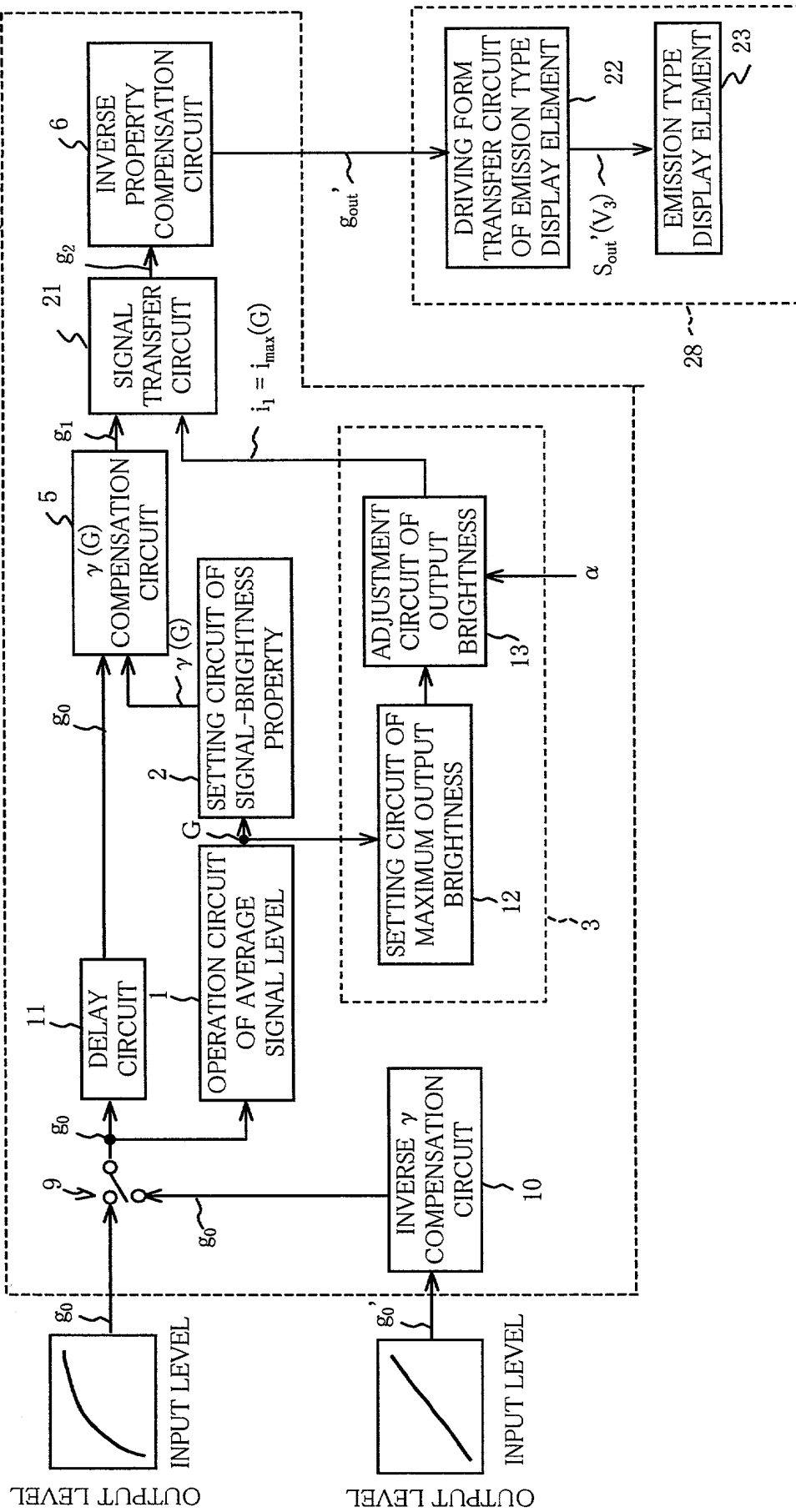


FIG 4



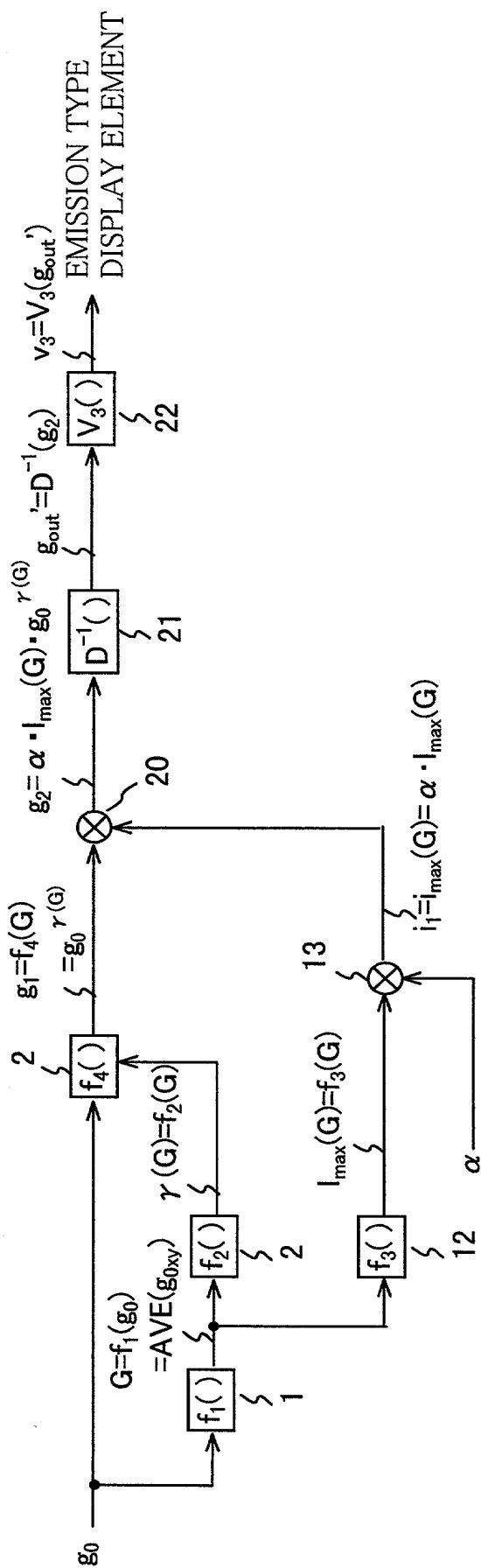


FIG 6

FIG 6 is a schematic diagram of a system 100 for processing input signals.

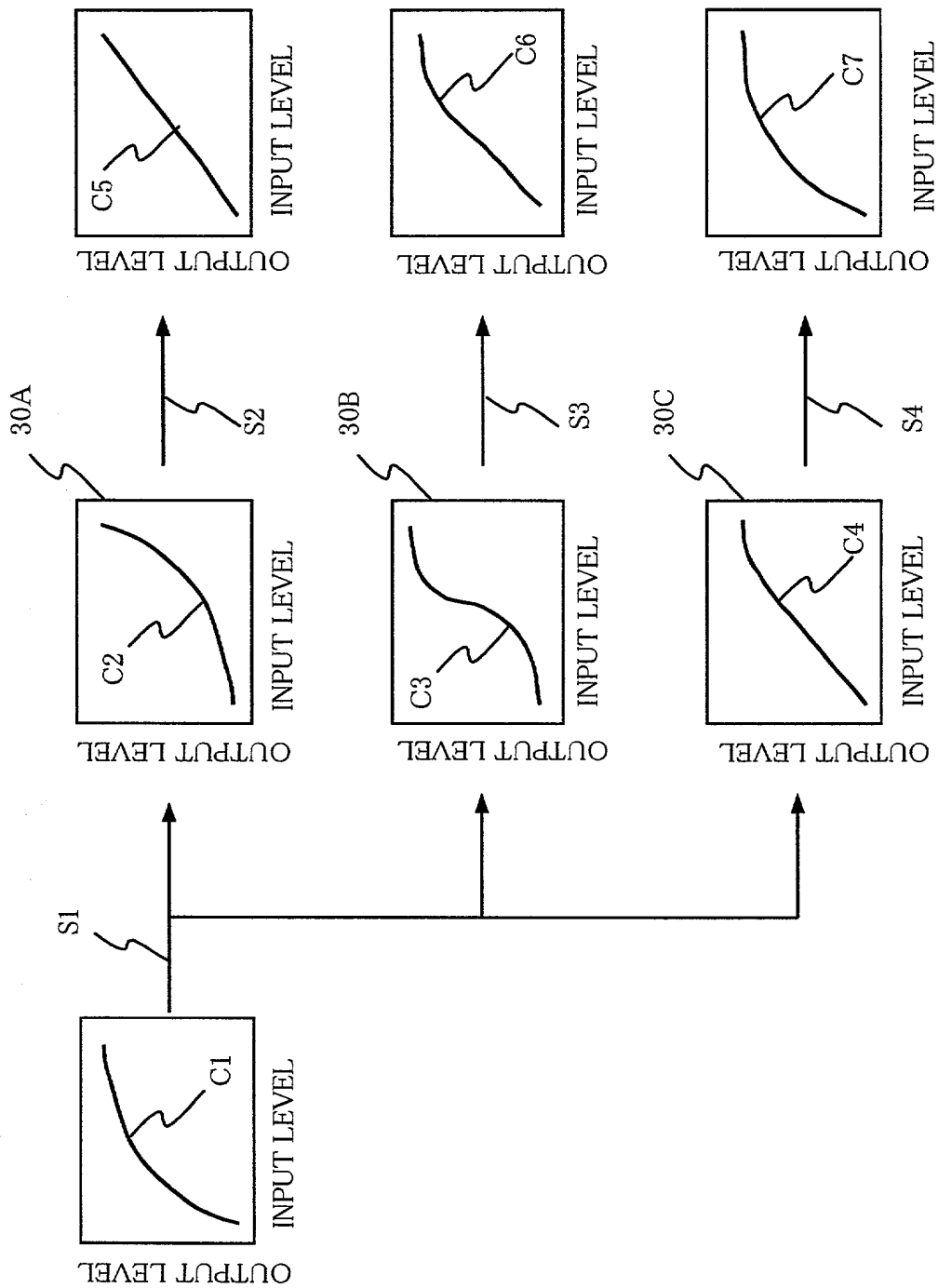


FIG 7

FIG 7 is a schematic diagram of a system 300 for processing input signals. The system 300 includes a plurality of input signals (S5, S6, S7, S8) and a plurality of processing blocks (C8, C9, C10, C11, C12, C13, C14). The input signals are processed by the processing blocks to produce output signals (O8, O9, O10, O11, O12, O13, O14). The processing blocks are arranged in a hierarchical structure, with C8 and C9 being the first level, C10 and C11 being the second level, and C12, C13, and C14 being the third level. The output signals are then combined to produce a final output signal (O15).

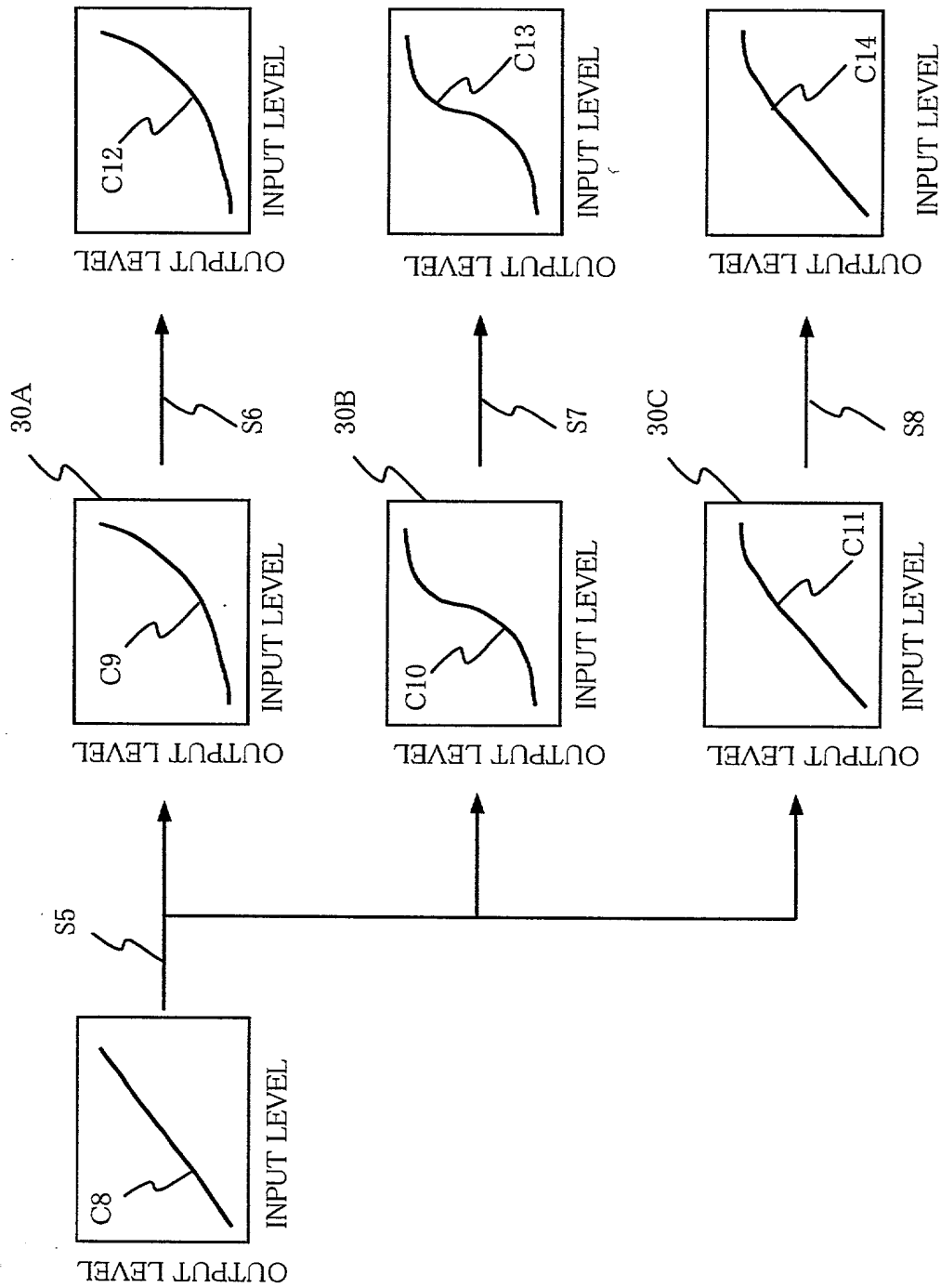


FIG 8

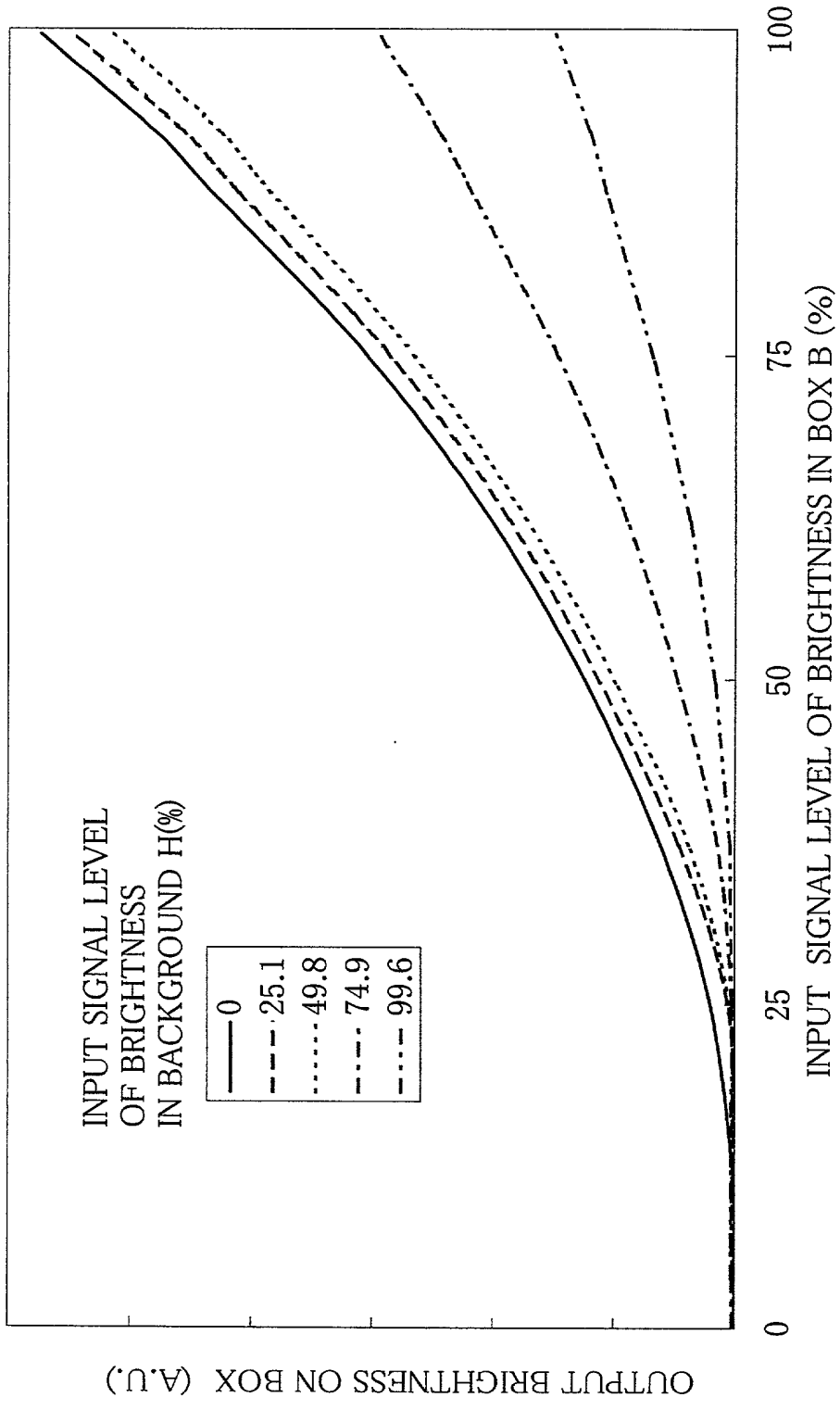




FIG 9

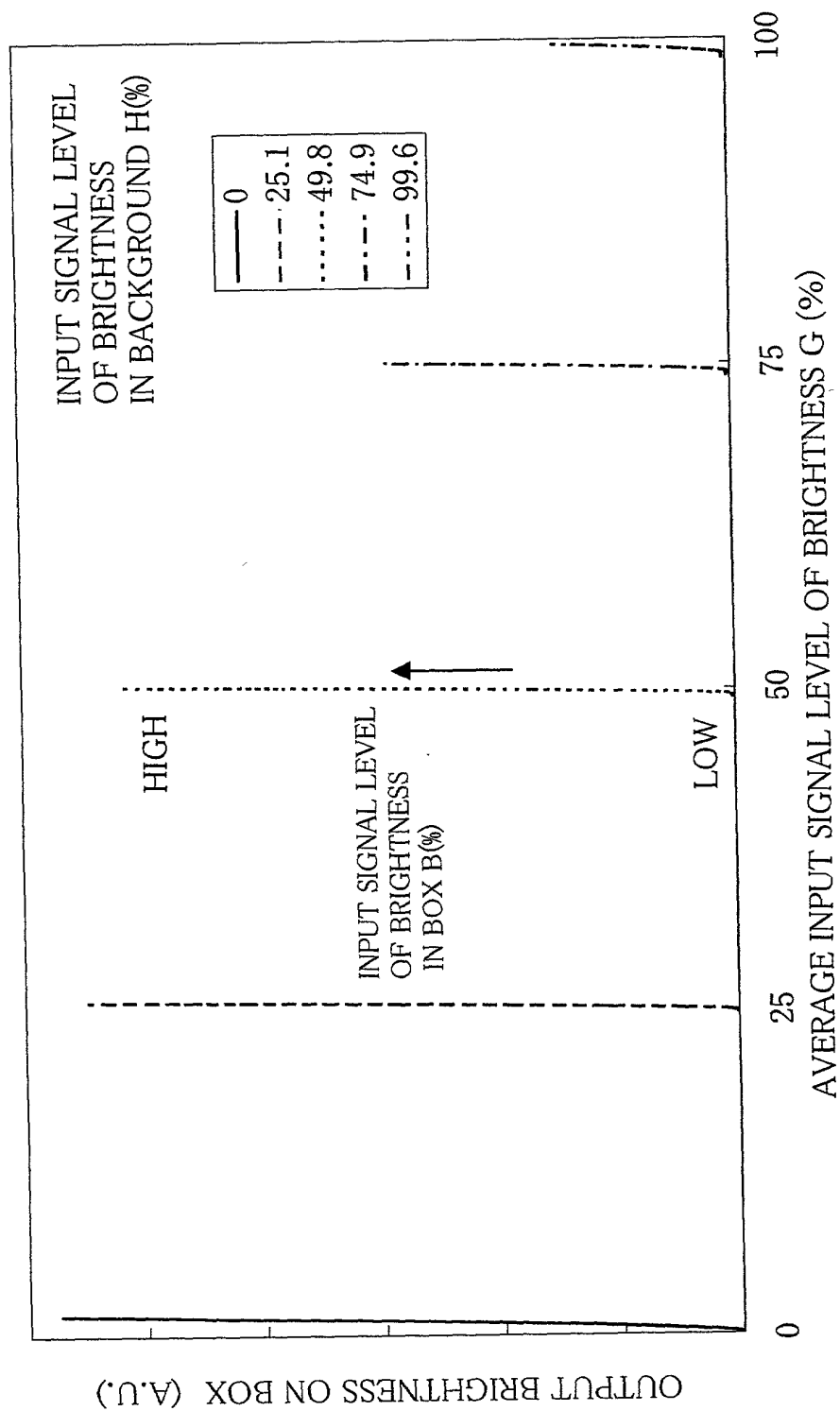


FIG 10

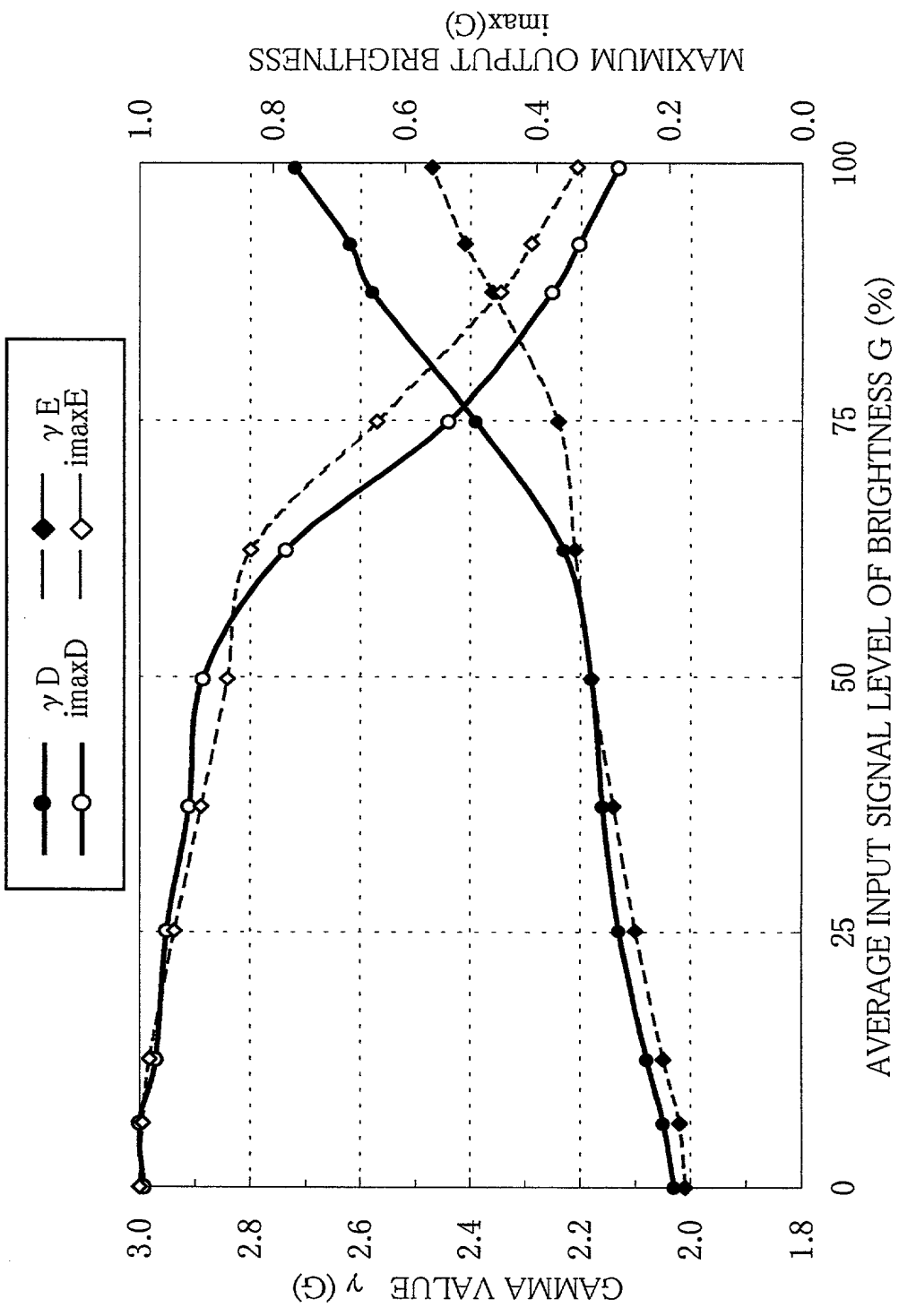


FIG 11

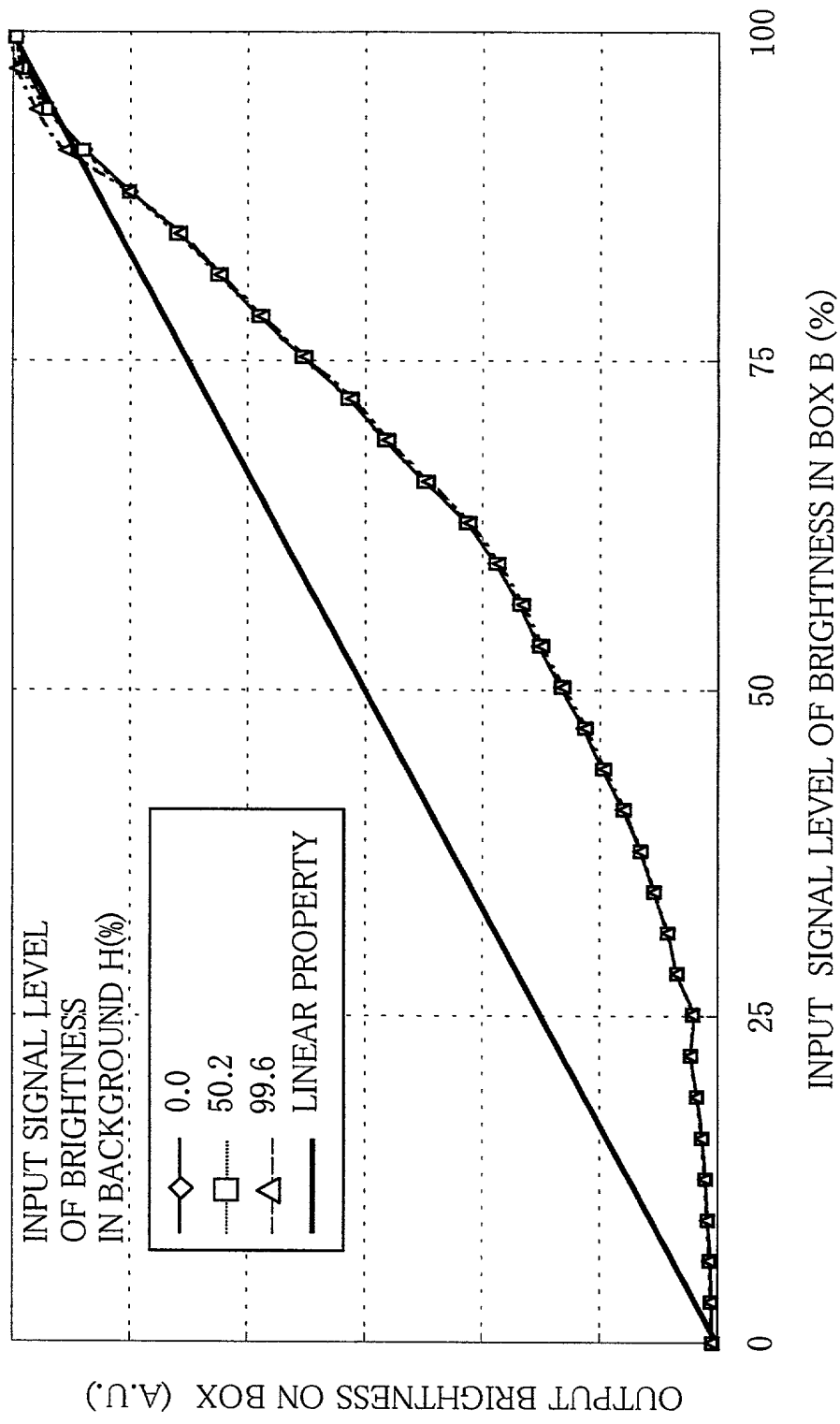


FIG 12

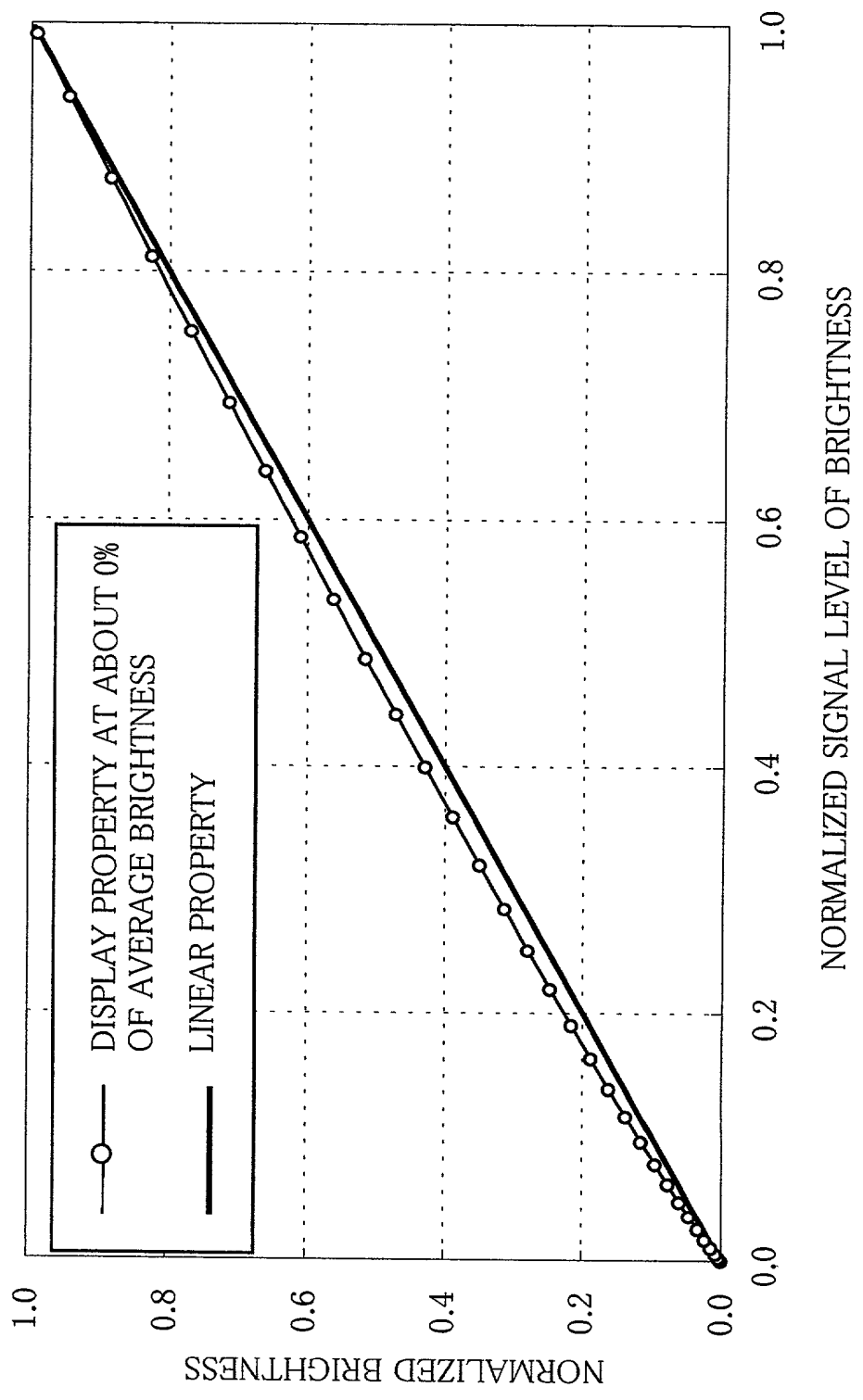


FIG 13

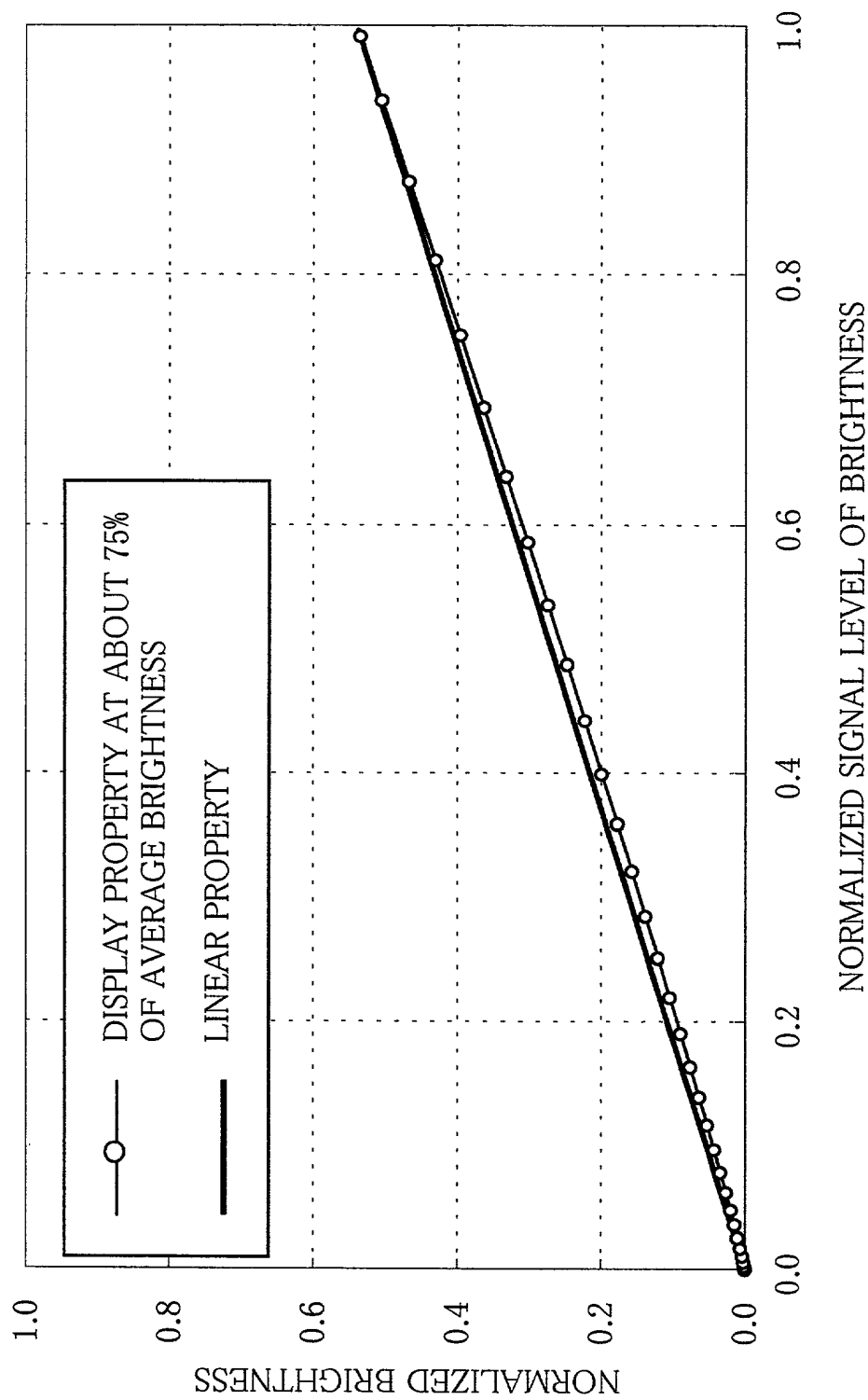


Figure 14 is a graph showing the relationship between Normalized Brightness and Normalized Signal Level of Brightness. The graph compares the Display Property (represented by a line with open circles) and the Linear Property (represented by a solid line). The Display Property curve is slightly non-linear, while the Linear Property is a straight line.

FIG 14

